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**Amendments to the Claims:**

This listing of claims will replace all prior versions, and listings, of claims in the application:

**Listing of Claims:**

Claim 1 (cancel)

Claim 2 (currently amended):        The spatial light modulator of claim [[1]] 4 wherein all of the pixels of the memory array are disposed outside the display array.

Claim 3 (cancel)

Claim 4 (original):    A The spatial light modulator of claim 3 wherein comprising: a multi-pixel display array on a first die, a multi-pixel memory array on a second die separate from the first die, the multi-pixel memory array having pixel storage cells, and wherein at least some pixels of the multi-pixel memory array are disposed outside the multi-pixel display array such that the multi-pixel memory array is physically decoupled from the multi-pixel display array;

at least one local pulse width modulation drive circuit coupled to at least one of the pixel storage cells;

a global counter coupled to the local pulse width modulation drive circuit to provide a global count value thereto, wherein:

the display pixels of the multi-pixel display array comprise first display pixels of a first color, and second display pixels of a second color;

the global counter includes,

a first global counter coupled to the local pulse width modulation drive circuits of the first display pixels, and

a second global counter coupled to the local pulse width modulation drive circuits of the second display pixels.

Claim 5 (previously presented):        The spatial light modulator of claim 4 wherein:

the display pixels of the multi-pixel display array further comprise third pixels of a third color.

Claim 6 (previously presented): The spatial light modulator of claim 5 wherein:  
the global counter further includes,  
a third global counter coupled to the local pulse width modulation drive circuits of the third display pixels.

Claim 7 (currently amended): The spatial light modulator of claim [[3]] 4 wherein:  
the multi-pixel display array includes display pixels of at least two different colors; and  
the global counter is adapted to count up to two respective different values and is  
switchably coupled to the respective different color display pixels to provide global counter  
values to their local pulse width modulation drive circuits in a time-slice manner.

Claim 8 (previously presented): The spatial light modulator of claim 7 wherein:  
the multi-pixel display array includes display pixels of three different colors.

Claim 9 (canceled)

Claim 10 (cancel)

Claim 11 (canceled)

Claims 12 - 18 (cancel)

Claim 19 (currently amended): ~~The A~~ method of manufacturing a light modulator,  
the method claim 18 further comprising:

constructing, on a first die and in a first area of the light modulator, a pixel display array  
including multiple display pixels, and constructing, on a second die separate from the first die  
and in a second area of the light modulator that is physically decoupled and substantially  
non-overlapping with the first area, a pixel memory array including multiple pixel storage cells;

constructing, within each of a plurality of the display pixels, a pulse width modulation driver circuit;

constructing a counter having an output coupled to each of the plurality of display pixels, and constructing, within each of the pulse width modulation driver circuits, a comparator having a first input coupled to the output of the counter and a second input coupled to receive a pixel data value from the pixel memory array, configuring the comparator to determine whether the pixel data value is greater-than-or-equal-to the counter output; and

constructing a lookup table to provide non-linear response in the pulse width modulation.

Claim 20 (canceled)

Claim 21 (previously presented): A method of operating a light modulator comprising:

performing a digital function on a pixel data value and a present counter value to generate one of a first result or a second result wherein a pixel memory array is physically decoupled from a pixel display array to hold the pixel data value;

in response to the first result, activating a pixel cell;

in response to the second result, deactivating the pixel cell;

detecting that a pixel memory cell in the pixel memory array is not operating correctly;

and, responsively

logically replacing that pixel memory cell with a redundant memory cell.

Claim 22 (previously presented): The method of claim 21 wherein:  
the digital function comprises a comparison.

Claim 23 (previously presented): The method of claim 21 further comprising, over time:

incrementing the counter value from 0 to N-1, wherein N is a number of bits of color depth represented in the pixel data value; and then  
wrapping back to 0.

Claim 24 (canceled)

Claim 25 (previously presented): The method of claim 21 further comprising:  
performing non-linear pulse width modulation.

Claim 26 (previously presented): The method of claim 21 wherein:  
the digital function is performed outside the pixel cell.

Claim 27 (previously presented): The method of claim 21 wherein:  
the digital function comprises using the present counter value to index into a lookup  
table.

Claims 28-33 (canceled)

Claim 34 (previously presented): A projection device comprising:  
a polarization beam splitter;  
a first light modulator coupled in optical contact with the polarization beam splitter, the  
first light modulator including,  
a first pixel display array in a first region of the first light modulator, and  
a first pixel memory array in a second region substantially not overlapping the first region  
of the first light modulator, such that at least a plurality of pixel memory cells of the first pixel  
memory array lie outside the first region of the first light modulator and the first and second  
regions are not physically coupled; and  
a second light modulator coupled in optical contact with the polarization beam splitter,  
the second light modulator including,  
a second pixel display array in a first region of the second light modulator, and  
a second pixel memory array in a second region substantially not overlapping the first  
region of the second light modulator, such that at least a plurality of pixel memory cells of the  
second pixel memory array lie outside the first region of the second light modulator.

Claims 35 - 43 (canceled)

Claims 44 -45 (cancel)

Claim 46 (previously presented): An article of manufacture comprising:  
a machine-accessible medium including data that, when accessed by a machine system,  
cause the machine system to perform the method of claim 21.

Claim 47 (previously presented): The article of manufacture of claim 46 wherein the  
machine-accessible medium further includes data that, when accessed by the machine system,  
cause the machine system to perform the method of claim 22.

Claim 48 (currently amended): The spatial light modulator of claim [[1]] 4, wherein  
the spatial light modulator comprises a liquid crystal on silicon display.

Claim 49 (cancel)

Claim 50 (previously presented): The projection device of claim 34, wherein the first  
light modulator comprises a liquid crystal on silicon display.

Claim 51 (currently amended): The spatial light modulator of claim [[1]] 4, wherein  
each of the pixel storage cells is associated with one pixel of the multi-pixel display array.

Claim 52 (cancel)

Claim 53 (previously presented): The projection device of claim 34, wherein each of  
the pixel memory cells of the first pixel memory array is associated with one pixel display cell of  
the first pixel display array.

Claim 54 (currently amended): The spatial light modulator of claim [[1]] 4, wherein  
the first die is formed using a first semiconductor technology and the second die is formed using  
a second semiconductor technology.